

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

EV301026271US

Applicants: James H. Schaffner, et al.) Re: Claim to Priority
)
)
Serial No.: Not yet assigned)
Filed: concurrently herewith)
)
Divisional of USSN 10/154,580)
filed on May 23, 2002) Our Ref: B-4032DIV2 621826-1
)
For: "STACKED GATE FLASH MEMORY)
DEVICE AND METHOD OF)
FABRICATING THE SAME") Date: April 16, 2004

35 U.S.C. 119 CLAIM TO PRIORITY

MAIL STOP PATENT APPLICATIONS
Commissioner of Patents and Trademarks
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

- [X] Applicants hereby make a right of priority claim under 35 U.S.C. 119(e) for the benefit of the filing date(s) of the following corresponding provisional application(s):

<u>COUNTRY</u>	<u>FILING DATE</u>	<u>SERIAL NUMBER</u>
U.S.A.	August 30, 2001	60/316,665

- [X] The subject application is a divisional application of U.S. Serial No. 10/154,580, which claims priority to the above-identified U.S. Provisional Application.

Respectfully submitted,



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